

Notice of References Cited	Application/Control No. 10/683,929		Applicant(s)/Patent Under Reexamination RAPP ET AL.	
	Examiner David J. Huisman		Art Unit 2183	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,909,565	06-1999	Morikawa et al.	712/200
*	B	US-5,752,071	05-1998	Tubbs et al.	712/34
*	C	US-4,862,407	08-1989	Fette et al.	712/35
*	D	US-5,185,871	02-1993	Frey et al.	712/205
*	E	US-6,624,819	09-2003	Lewis, Michael C.	345/561
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Hennessy and Patterson, "Computer Architecture - A Quantitative Approach, 2 nd Edition," 1996, Morgan Kaufman Publishers, Inc., pp.134,155			
	V	Free Online Dictionary of Computing (FOLDOC), definitions of "FPGA" and "raw data", 1997, http://foldoc.org			
	W				
	X				

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.